

Fig. 2

Fig. 3

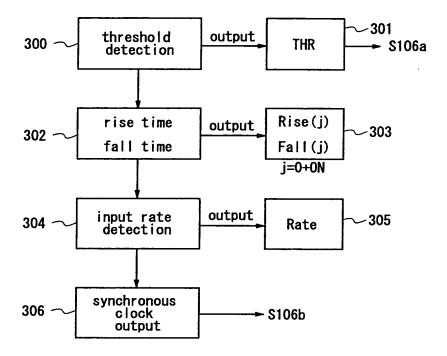
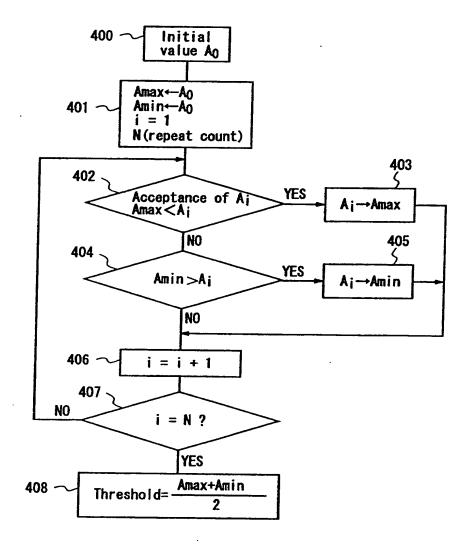


Fig. 4



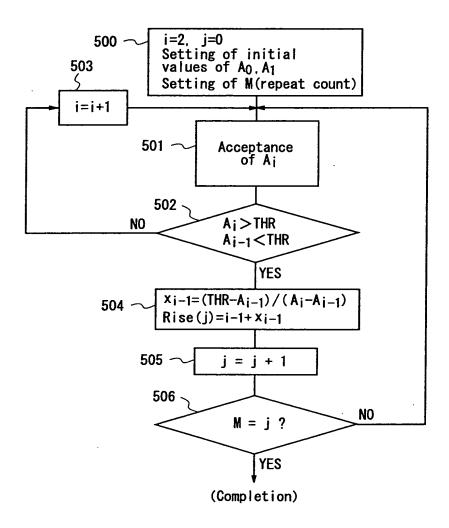


Fig. 6

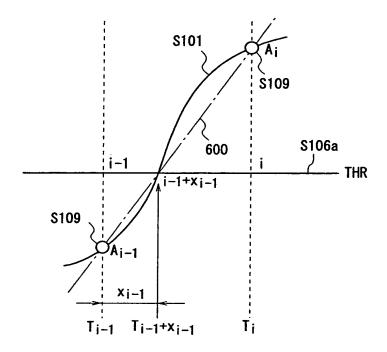
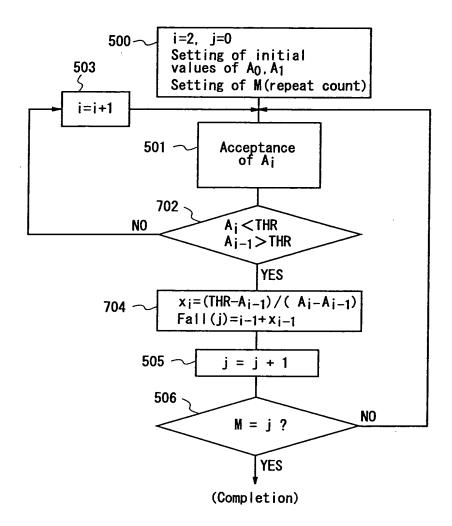


Fig. 7



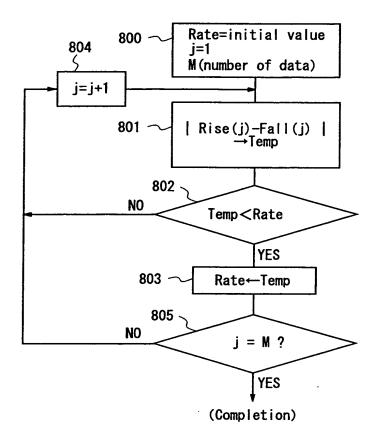
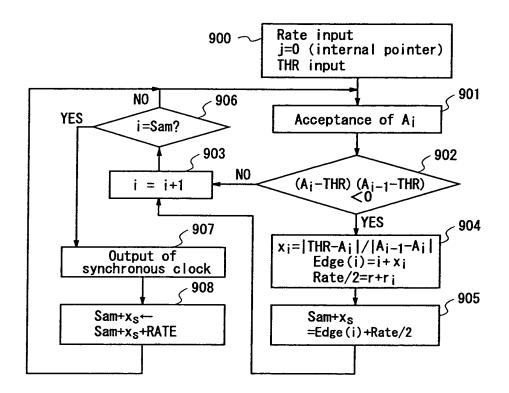


Fig. 9



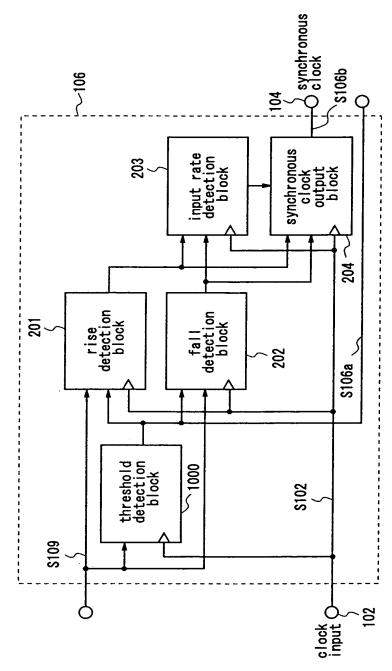
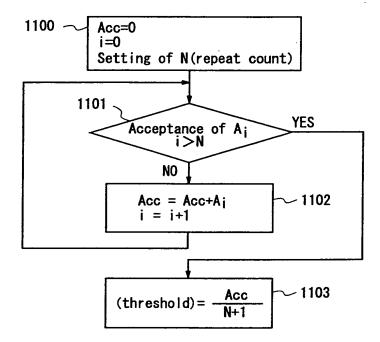


Fig. 10

Fig. 11



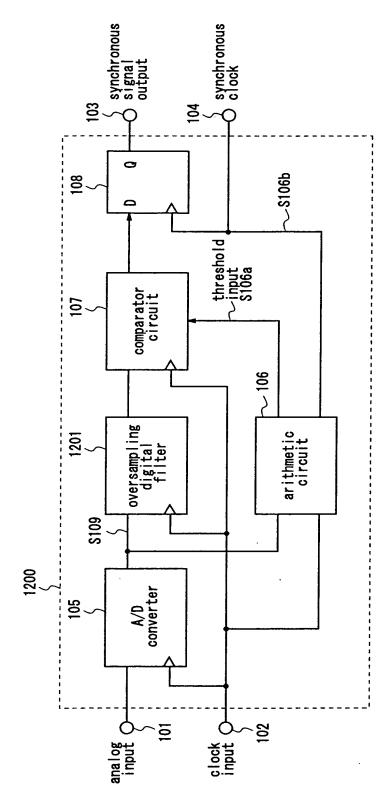


Fig. 12

> synchronous signal output 1304 Synchronous 1303 \$1303 O \$1304 1310 \$1305 edge detector **S1308** 1309 1308 1305 comparator circuit counter decoder 1307 1300 \$1301 **S1306** 1302 | \$1302 threshold O-input analog input

Fig. 13 Prior Art